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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

in re Application of: Tzu-Jin Yeh et al. Confirmation No. 3805

Serial No.: 10/632,456

Group Art Unit: 2811

Filed: 07/31/2003

Examiner: Gene M. Munson

For:

JAN 1 1 2005

INDUCTOR Q VALUE IMPROVEMENT

I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as "Express Mail Post Office to Addressee" Mailing Label Number EV175960980US to: Mail Stop Non-Fee Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

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RESPONSE TO RESTRICTION REQUIREMENT

Dear Sir:

This is in response to the Restriction Requirement dated December 23, 2004. The Examiner requires an election between Group I: Claims 1-25, drawn to a semiconductor device, classified in class 257, subclass 531 or Group II: Claims 25-41, drawn to a process for making semiconductor devices, classified in class 438, subclass 514.

Applicant hereby elects Group I, claims 1-25. This election is made without traverse.

Respectfully submitted,

Steven E. Koffs

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